

Organizacija računalnikov

Laboratorijske vaje

R.Rozman 2020

Vsebina vaj

- **Programiranje v zbirnem jeziku ARM**
 - ponovitev, razširitev, nadgradnja
 - *Prvo preverjanje (predvidoma druga polovica novembra)*
- **Vhodno izhodne naprave, FRI-SMS**
 - Paralelni vhod/izhod (**PIO**)
 - Časovniki (**TC**)
 - Zaporedni vmesnik (**UART, DBGU**)
 - Prekinitve, prekinitveni krmilnik (**AIC**) * <- 2020: *Praktična vaja STM Discovery*
 - *Zaključno preverjanje – zadnji teden semestra*
- priključitev osnovnih V/I enot, osnove jezika C (neobvezno)
- **2 obvezni in 2 neobvezni domači nalogi**
 - MiMo, osnovna ARM-Aplikacija, npr. oddajnik Morsejeve abecede
 - MiMo dodatki, ARM razširitve (aplikacija, senzorji)
- ***Neobvezni seminar po dogovoru z asistentom***

Ocenjevanje*

- Vaje prispevajo **50%** h končni oceni in morajo biti opravljene:
 - uspešno oddane domače naloge (obvezni del),
 - preverjanja morajo biti **vsaj 50% oz. 100 točk**:
 - dve preverjanji ($90 + 110$)
 - možnih 200 točk
 - vsaj 100 točk na obeh skupaj
 - dodatno delo (neobvezne domače naloge, projekt, MiMo,...) se prišteje točkam iz preverjanj
 - *2020: Boljša priprava na 2. preverjanje*

* Zaradi Covid situacije se lahko še prilagodi

Razvojno okolje WinIDEA



The screenshot displays the WinIDEA development environment interface. The main window shows assembly code in the .text section:

```
.text
NIZ1: .asciz "Timi Zajc je svetovni prvak v smucarskih skokih!"
NIZ2: .space 20
.align
NASLOVI_R: .space 40
.align 1
STEVILO_R: .space 2
```

The Disassembly view shows the generated assembly code:

| Address | Data | Disassembly |
|-----------|------|--------------------------------|
| 00007C004 | | <code>start</code> |
| 00007C004 | | <code>adr r0, NIZ1</code> |
| 00004F504 | | <code>sub r0,pc,#7C</code> |
| 00004F504 | | <code>adr r5, NIZ2</code> |
| 00003C604 | | <code>sub r5,pc,#4F</code> |
| 00003C604 | | <code>adr r6, NASLOVI_R</code> |
| 00003C604 | | <code>sub r6,pc,#3C</code> |
| 000018704 | | <code>adr r7, STEVILO_R</code> |
| 000018704 | | <code>sub r7,pc,#18</code> |

The Registers window shows all general-purpose registers initialized to zero.

The Memory dump window shows the first 32 bytes of memory starting at address 0x00000000:

| Address | Value | Content |
|----------|----------------------------------|---------|
| 00000000 | 23 00 00 EA 22 00 00 EA #..." | |
| 00000008 | 21 00 00 EA 20 00 00 EA !... . | |
| 00000010 | 1F 00 00 EA 1E 00 00 EA | |
| 00000018 | 1D 00 00 EA 1C 00 00 EA | |
| 00000020 | 54 69 6D 69 20 5A 61 6A Timi Zaj | |
| 00000028 | 63 20 6A 65 20 73 76 65 c ie sve | |

The Output window shows the compilation process:

```
Compiling ...
crt0.s
user.s
Linking
"sample.elf (Dir:C:\Winidea\Delo\RA_Sim\Debug\)" ... was successfully generated
0 Error(s) 0 Warning(s)
```

The bottom status bar indicates the application is Ready.

The image also includes a photograph of the WinIDEA software box and a physical hardware development board. The board has a color LCD screen displaying a car's instrument cluster with various gauges and data. A ribbon cable connects the board to a blue circuit board above it.

V drugem delu – FRI SMS



AT91SAM9260

